

# C3D20065D

## Silicon Carbide Schottky Diode

### Z-REC® RECTIFIER

$V_{RRM}$	=	650 V
$I_F(T_c=135^\circ C)$	=	26 A**
$Q_c$	=	48 nC**

#### Features

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on  $V_F$

#### Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

#### Applications

- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter stages
- AC/DC converters

#### Maximum Ratings ( $T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{RRM}$	Repetitive Peak Reverse Voltage	650	V		
$V_{RSM}$	Surge Peak Reverse Voltage	650	V		
$V_{DC}$	DC Blocking Voltage	650	V		
$I_F$	Continuous Forward Current (Per Leg/Device)	27.5/55 13/26 10/20	A	$T_c=25^\circ C$ $T_c=135^\circ C$ $T_c=149^\circ C$	Fig. 3
$I_{FRM}$	Repetitive Peak Forward Surge Current	46* 31*	A	$T_c=25^\circ C, t_p = 10 \text{ ms, Half Sine Wave}$ $T_c=110^\circ C, t_p = 10 \text{ ms, Half Sine Wave}$	
$I_{FSM}$	Non-Repetitive Peak Forward Surge Current	90* 71*	A	$T_c=25^\circ C, t_p = 10 \text{ ms, Half Sine Wave}$ $T_c=110^\circ C, t_p = 10 \text{ ms, Half Sine Wave}$	Fig. 8
$I_{FSM}$	Non-Repetitive Peak Forward Surge Current	860* 680*	A	$T_c=25^\circ C, t_p = 10 \mu\text{s, Pulse}$ $T_c=110^\circ C, t_p = 10 \mu\text{s, Pulse}$	Fig. 8
$P_{tot}$	Power Dissipation (Per Leg/Device)	115.5/231 50/100	W	$T_c=25^\circ C$ $T_c=110^\circ C$	Fig. 4
$dV/dt$	Diode $dV/dt$ ruggedness	200	V/ns	$V_R=0-650V$	
$T_j, T_{stg}$	Operating Junction and Storage Temperature	-55 to +175	°C		
	TO-247 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

\* Per Leg, \*\* Per Device

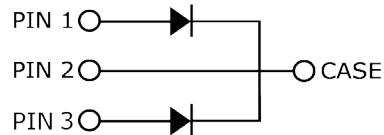
#### Package



TO-247-3



AEC-Q101 Qualified



Part Number	Package	Marking
C3D20065D	TO-247-3	C3D20065

## Electrical Characteristics (Per Leg)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_F$	Forward Voltage	1.5 2.0	1.8 2.4	V	$I_F = 10 \text{ A } T_J = 25^\circ\text{C}$ $I_F = 10 \text{ A } T_J = 175^\circ\text{C}$	Fig. 1
$I_R$	Reverse Current	12 24	60 220	$\mu\text{A}$	$V_R = 650 \text{ V } T_J = 25^\circ\text{C}$ $V_R = 650 \text{ V } T_J = 175^\circ\text{C}$	Fig. 2
$Q_C$	Total Capacitive Charge	24		nC	$V_R = 400 \text{ V}, I_F = 10 \text{ A}$ $di/dt = 500 \text{ A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$	Fig. 5
C	Total Capacitance	460.5 44 40		pF	$V_R = 0 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$ $V_R = 200 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$ $V_R = 400 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	Fig. 6
$E_C$	Capacitance Stored Energy	3.6		$\mu\text{J}$	$V_R = 400 \text{ V}$	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

## Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.3** 0.65*	$^\circ\text{C}/\text{W}$	Fig. 9

\*\* Per Leg, \* Both Legs

## Typical Performance (Per Leg)

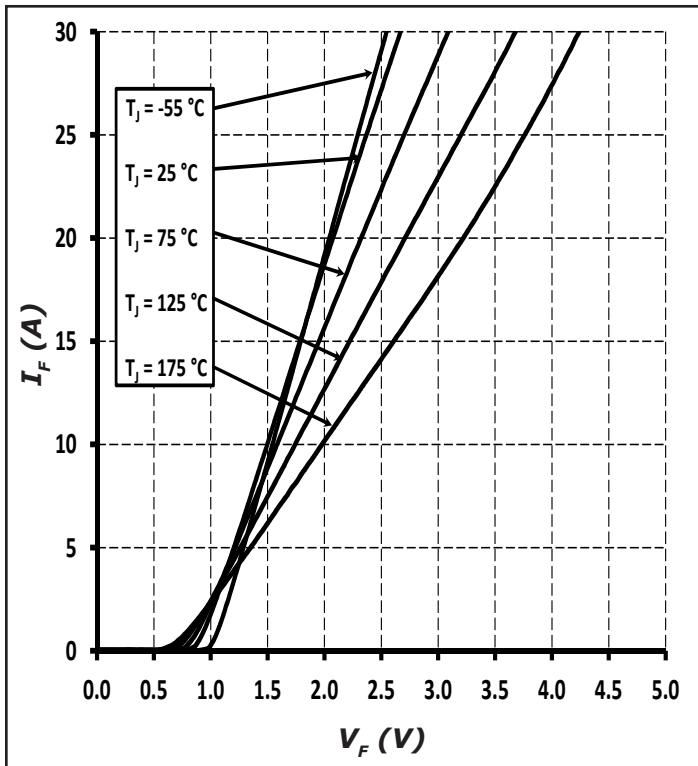


Figure 1. Forward Characteristics

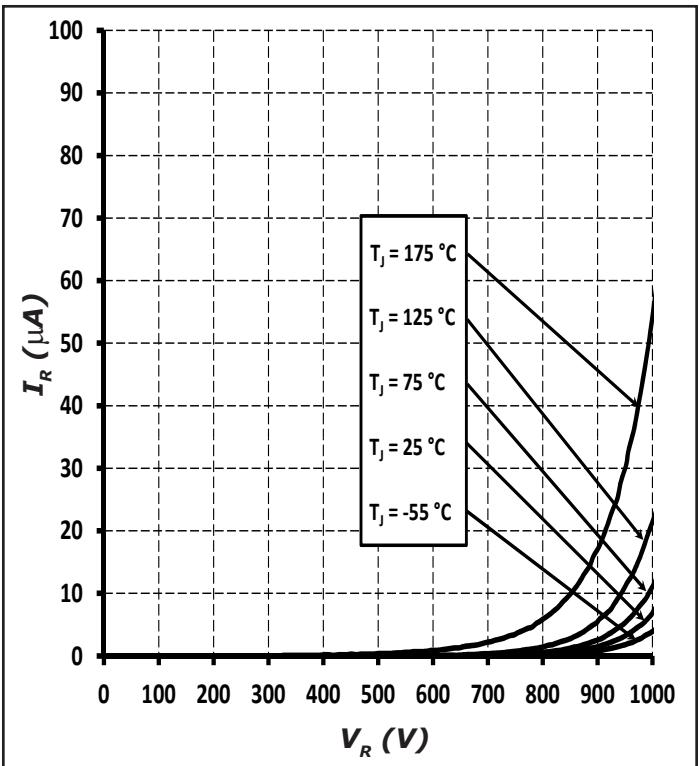


Figure 2. Reverse Characteristics